

H1046 U.S. PTO  
09/021556  
03/29/01

Class	Subclass
	

**ISSUE CLASSIFICATION**

PATENT NUMBER

## U.S. UTILITY Patent Application

O.I.P.E.		PATENT DATE	
SCANNED	Q.A.		

APPLICATION NO. 09/821556	CONT/PRIOR	CLASS 711 713	SUBCLASS 1	ART UNIT 2185 2183	EXAMINER lee
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## APPLICANTS

Terry Kendall

2115

CANNOLLY

**TITLE**

Non-volatile alterable default flash memory configuration registers

PTO-2040  
12/89

# ISSUING CLASSIFICATION

ORIGINAL				CROSS REFERENCE(S)						
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION										
				<input type="checkbox"/> Continued on Issue Slip Inside File Jacket						

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	_____ (Primary Examiner) (Date)			<b>ISSUE FEE</b>	
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